

Fig. 1

Figure 2

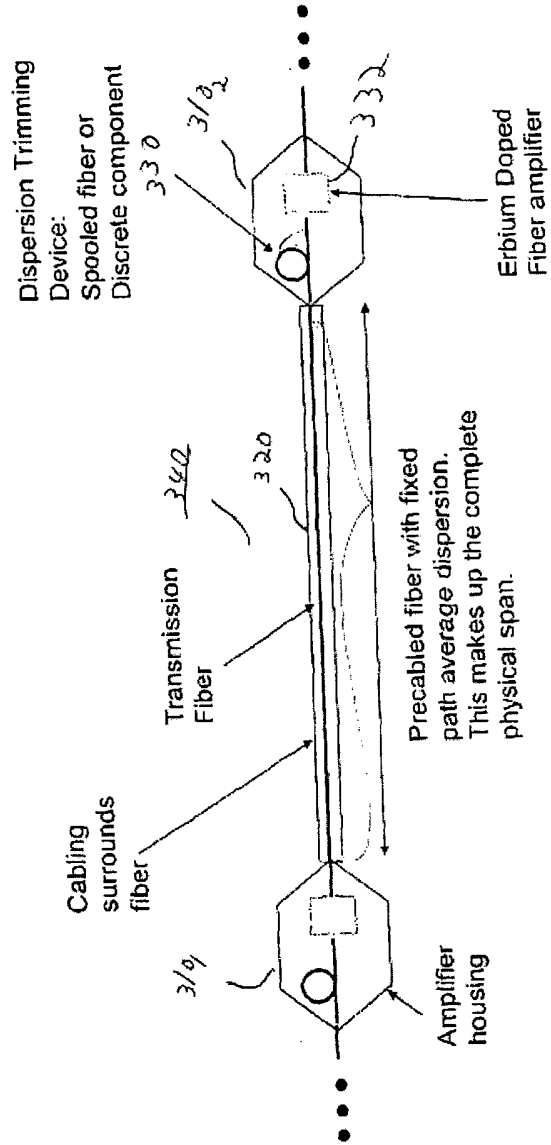
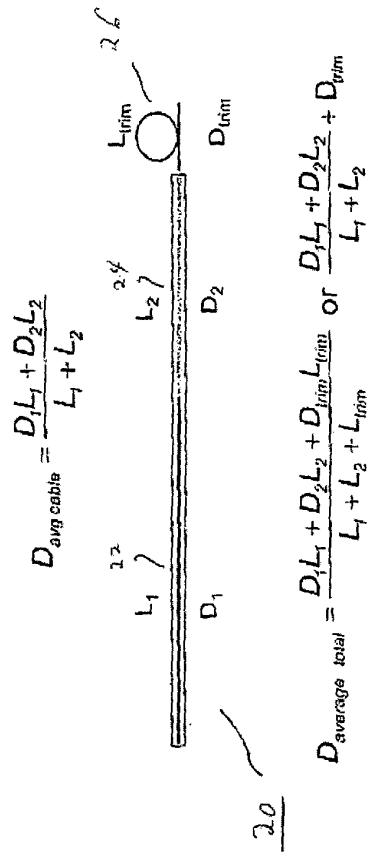


Figure 3



Where D_{lim} is the group delay (ps/nm) of a discrete device

Note: L_2 can be 0

Amplifier Design for Complex Dispersion Maps

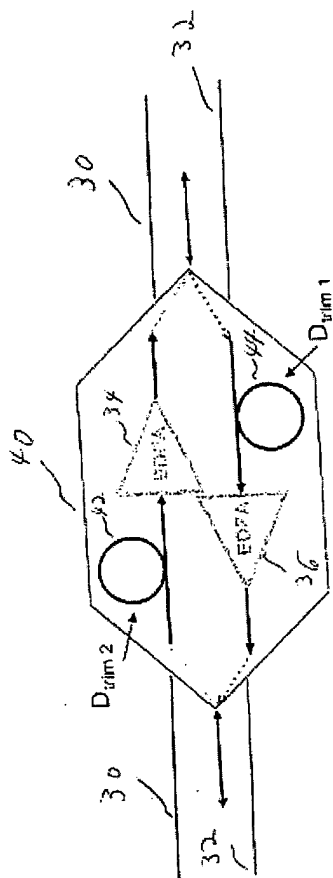


Figure 4